CS 250 Midterm 1

* Truth table for: or, and, not, nand, nor, xor, xnor.
* Operands: or, and, not
* Decoder is circuit for pointing
* Know: adders, decoders, demux/mux, latch, register, flip-flops
* Registers store bit strings (bit strings interpreted as instructions)
* Hardware that stores bits is memory (ie registers)
* Twos complement bounds: n-bits…. low -2n - 1  high 2n-1
* Ones complement bounds: n-bits…. low –(2n-1 – 1) high 2n-1
* Universal logic gates: NAND, NOR
* Know active high/low